PATENT APPLICATION

D. WYATI

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U	J.S. Patent Application)	
Applic	ant: Nigel Peter Topham	I hereby certify that this paper is being deposited with the United States Postal Service as EXPRESS MAIL in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on February 6, 2001	
Serial No.) Express Label No.: _EL.769181672 US) Signature:	
Filed:	February 6, 2001)	
	COMMUNICATING INSTRUCTION RESULTS IN PROCESSORS AND COMPILING METHODS FOR PROCESSORS	Cocococococococococococococococococococ	
Art Unit:)	

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants through counsel herewith submit copies of the publications as set forth in the attached form PTO-1449 as follows:

FOREIGN PATENT DOCUMENTS

DOCUMENT NO.	COUNTRY	PUBLICATION DATE
2276742	United Kingdom	Oct. 5, 1994
698846	Europe	Feb. 28, 1996

OTHER DOCUMENTS

1. *IBM Technical Disclosure Bulletin*; "Multisequencing a single instruction stream identifying dead registers in the common virtual register file"; Vol. 36, N. 4, pp. 441-444; April 1, 1993.

Applicants respectfully request that the Examiner consider the above-listed references in the examination of this application and list these references of record in the application.

Respectfully submitted,
GREER, BURNS & CRAIN, LTD.

By:

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February 6, 2001

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